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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No. : 10/684,812 Confirmation No. : 1700  
First Named Inventor : Koki ISHIZAKI  
Filed : October 15, 2003  
TC/A.U. : 2815  
Examiner : Sheila V. CLARK  
Docket No. : 056203.52845US  
Customer No. : 23911  
Title : The Multilayer Electronic Substrate, and The  
Method of Manufacturing Multilayer Electronic  
Substrate

**REPLY**

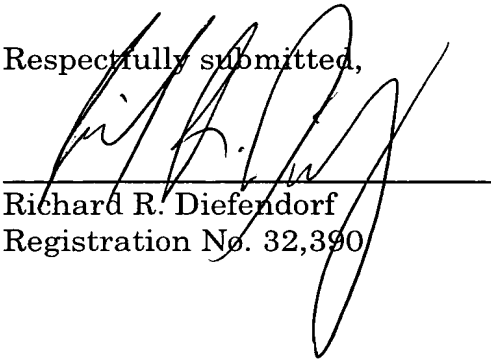
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the requirement set forth in the Office Action dated January 26, 2005, the invention identified as invention "I" by the Examiner, defined by claims 1-6 and 11-15, is elected, with traverse, for examination. Reconsideration of the requirement for restriction is requested, however, since the claims of inventions I and II share common features and should be examined together.

February 25, 2005

Respectfully submitted,

  
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